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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Sompong P. Olarig

Confirmation No.: 5912

Application No.: 09/769,953

Examiner: Le, Dieu

Filing Date:

01/25/2001

Group Art Unit: 2114

Title:

COMPUTER SYSTEM HAVING CONFIGURABLE CORE LOGIC CHIPSET FOR

CONNECTION TO A FAULT-TOLERANT ACCELERATED GRAPHICS PORT BUS AND

PERIPHERAL COMPONENT INTERCONNECT BUS

Mail Stop PGPUB Drawings **Commissioner for Patents** PO Box 1450 Alexandria, VA 22313-1450

DRAWING TRANSMITTAL LETTER

Sir:	
Enclosed here	with please find:
()	sheets of redlined drawing(s) which indicate proposed changes to the drawing(s). Upon approval of these proposed changes, formal drawing(s) will be submitted.
()	sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) which accompanied the Office Action dated
()	sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) and approved in the Notice of Allowability dated
(x) <u>28</u>	sheets of formal drawing(s). Please substitute these formal drawing(s) for the informal drawing(s) originally filed.
()	

Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

Respectfully submitted,

Sompong P. Olarig

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date of Deposit: 02/04/2005

Typed Name: Michael G. Fletcher

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